

# EE 491 WEEKLY REPORT 12

## Project title: Fast, Compact, High, Strength, Magnetic Pulse Generator

Group number: DEC1622

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Client &/Advisor: Mani Mina, Neelam Prabhu Gaunkar

Date: 18<sup>th</sup> April 2015

Team Leader: Wei Shen Theh

Team Webmaster: Wing Yi Lwe

Team Communication Leaders: Jia yu Hong

Key Concept Holder: Aqila-Sarah Zulkifli

## Weekly Summary

The team focused on three things for the week. One, the team worked on our presentation slides and had a practice session to ensure the presentation is well-paced and within the allocated period. The team also worked further on our design document and project plan version 2. However, these are considered still halfway done since we are still lacking several important data that we would like to include. We are also waiting for feedback on one of our previous documents.

The team spent the majority of the week working on getting our layout done. We stumbled upon multiple unexpected trouble and is trying to solve them one at a time. Most of the trouble come chiefly from getting footprints of our components into the layout program (either Multisim then Ultiboard or Eagle PCB). The team had requested help from our adviser with this and hope to get it resolve by the coming week considering how close we are to the demonstration week.

## Past week accomplishments

We practiced our presentation on Monday. We tried to create a layout in Multisim, but we could not find the matching footprints for our circuit components. We researched how to import the components' footprint into Multisim, but we could not find them and tried to do our own footprints from scratch. Jiayu started with downloading Eagle, and tried to create the layout. Although Neelam found out that Eagle had similar MOSFET footprints, we still had to look for similar footprints for diodes and the current sensitive resistor. We edited our design document 2 with reference to design document 1's feedback as we are preparing for our final presentation.

## Individual contributions

<u>NAME</u>	<u>Individual Contributions</u>	<u>Hours this week</u>	<u>HOURS cumulative</u>
Wei Shen Theh	Working on new design simulation and layout	6	58
Wing Yi Lwe	Edited the design document, helped with the weekly report	6	50
Jiayu Hong	Worked on layout in Multisim and Eagle, made sure members contributed on the weekly report and submitted the report	6.5	53
Aqila-Sarah Zulkifli	Edited the design document and helped with the weekly report	5	48

## Summary of weekly advisor meeting

Our weekly advisor meeting with Neelam focused on layout as she helped us with the Eagle software. Since none of us have any experience with it, we need to take a little more time to get used to it in order to finish the layout of the entire circuit. We were very thankful when Neelam managed to find the exact footprint for our MOSFET component. We will definitely be trying our best to hustle the layout process while simultaneously editing our final project plan and design document.

### Plan for coming week

Our plan for this coming week is to edit and add more results in our project plan and design document which is due on Saturday. Therefore we have to finish our layout as soon as possible so that we can include that too. Wing Yi will upload everything on the website after we have submitted every documents, project plans, and weekly reports. Besides that, we will be preparing for our final presentation which will be on next Tuesday at 11AM. Other than that, we will be having our regular group meeting on Monday and Friday evening to discuss more about the project and the presentation.